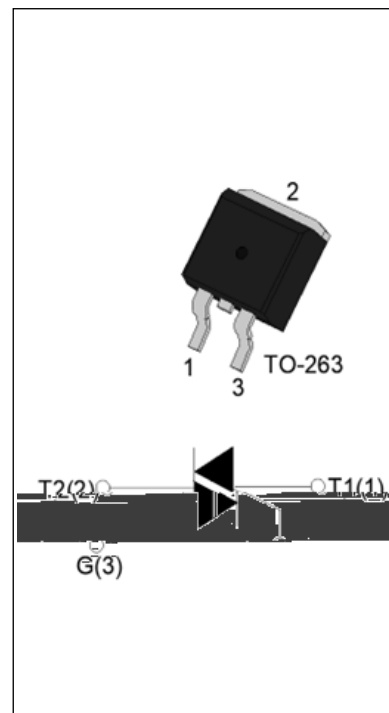


DESCRIPTION:

The JST24E-800CW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST24E-800CW snubberless triac is especially recommended for use on inductive loads. Package TO-263 is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	25	A
V_{DRM}/V_{RRM}	800	V
$I_{GT} / /$	35/35/35	mA



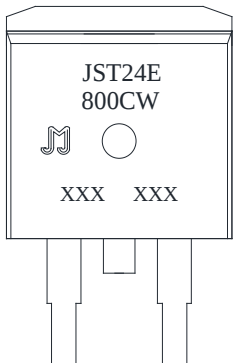
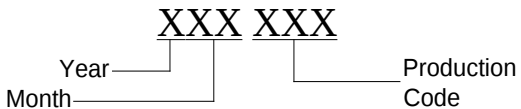
ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	800	V
RMS on-state current ($T_c 097^\circ\text{C}$)	$I_{T(RMS)}$	25	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	250	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		275	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	340	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)	di/dt	100	A/s
Peak gate current ($t_p=20\text{ s}$, $T_j=125^\circ\text{C}$)	I_{GM}	4	A
Average gate power dissipation ($T_j=125^\circ\text{C}$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W

ORDERING INFORMATION

J	ST	24	E	-800	CW	-/
JieJie Microelectronics Co., Ltd.	Triacs					Blank:Tube -TR:Tape & Reel
	IT(RMS):25A					
		E:TO-263			CW:IGT1-3 035 mA	
				800:VDRM /VRRM 1800V		

MARKING

 Diagram showing the marking on the JST24E-800CW package. The marking includes the part number JST24E 800CW, the JieJie logo, and two sets of XXX marking.	 Diagram showing the marking code breakdown. The code is XXX XXX. The first XXX is labeled Year and the second XXX is labeled Production Code. The Year is further broken down into Month and Year.
--	--

JST24E-800CW

Jie Jie iesw 1.376 0 Td [(7 (J5 1 Tf 0 Tw4 (C

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

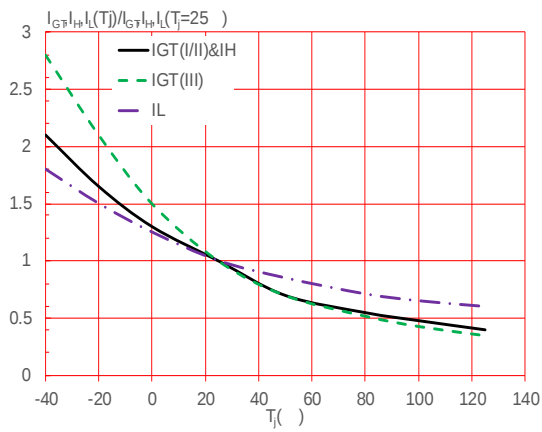
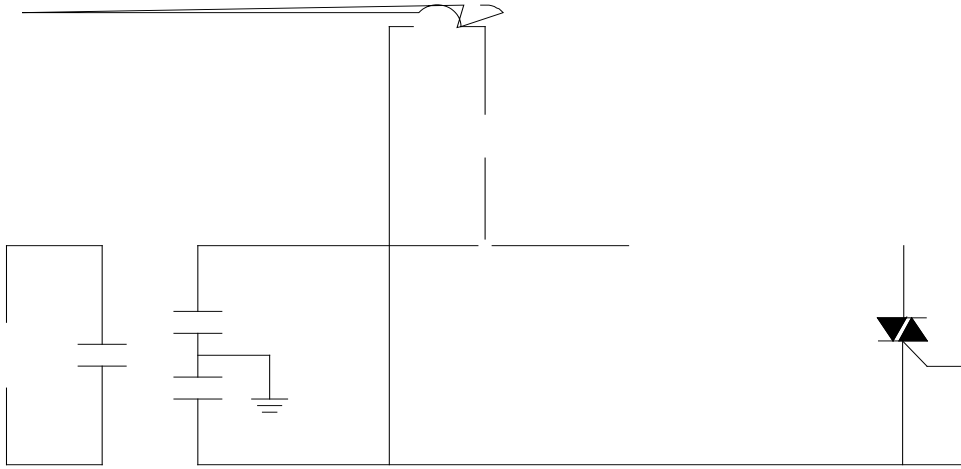


FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



JST24E-800CW

DELIVERY MODE



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.



is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.