



JST20C-600BW 20A TRIAC

A.1..4. (1)

Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	3.8	kV
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ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33\Omega$	- -	MAX.	50	mA
V_{GT}		- -	MAX.	1	V

ORDERING INFORMATION

J	ST	20	C	-600	BW
JieJie Microelectronics Co., Ltd.	Triacs	IT(RMS):20A	C:TO-220C	600:VDRM /VRRM 600V	BW:IGT1-3 50mA

RM KM (NB:I

FIG.1: Maximum power dissipation versus RMS on-state current

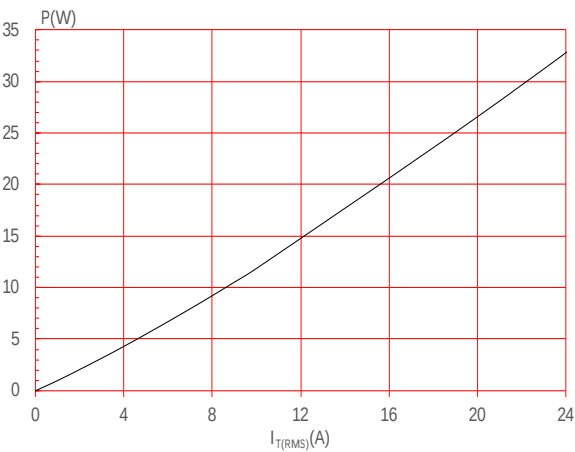


FIG.2: RMS on-state current versus case temperature

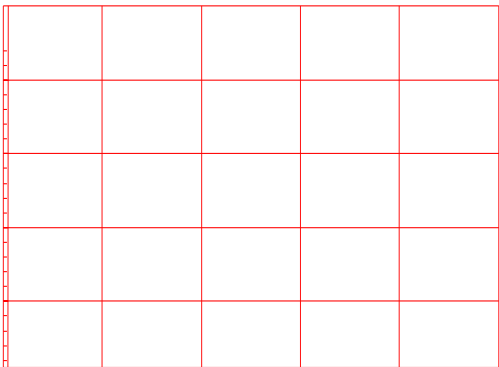
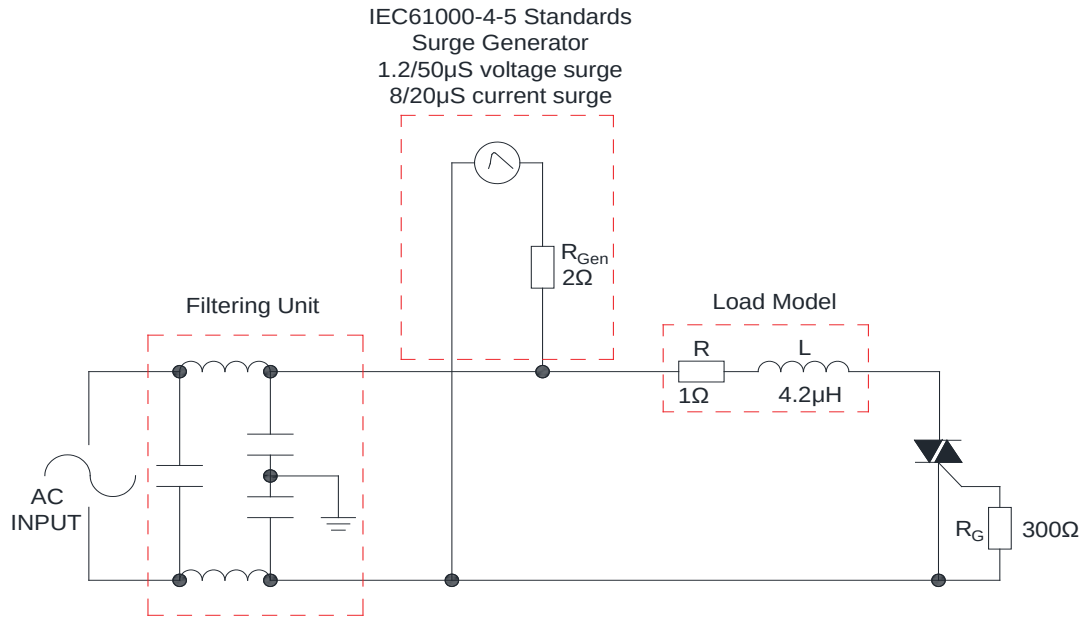


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics

ORDERING INFORMATION

Order code	Voltage V _{DRM} /V _{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
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