

JIEJIE MICROELECTRONICS CO., LTD.

JST16E-1200TW 16A TRIAC

Rev.A.1.1

12 39.6 Γm4[(10)13.12]T.0.11

Peak pulse voltage
($T_j=25$)

ORDERING INFORMATION

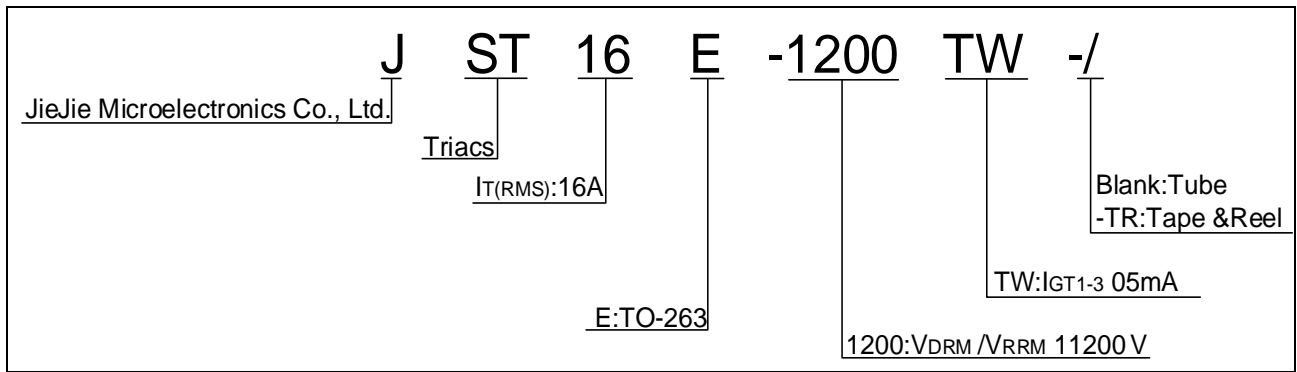


FIG.1: Maximum power dissipation versus RMS on-state current



FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

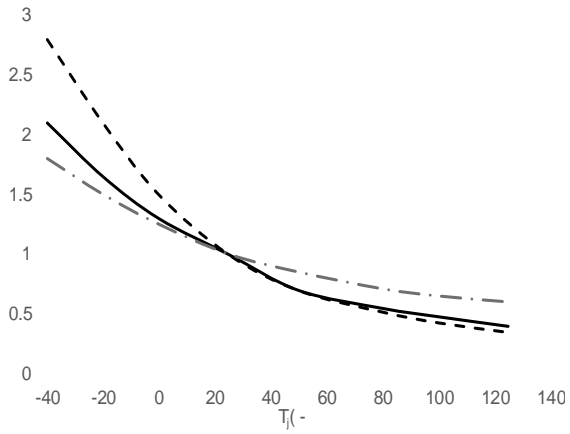


FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			
JST16E-1200TW	1200	5	TO-263	50	Tube
JST16E-1200TW-TR				800	Tape & Reel

Document Revision History

Date	Revision	Changes
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DELIVERY MODE

