

Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	1	kV
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($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value	Unit	
I_{GT}	$V_D=12V$ $R_L=33$	- -	MAX.	50	mA
				70	
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	- -	MAX.	70	mA
				100	
I_H	$I_T=500mA$		MAX.	60	mA
dV/dt	$V_D=540V$ Gate Open $T_j=125$		MIN.	1000	V/ μs
(dV/dt) _c	(dI/dt) _c =7A/ms, $T_j=125$		MIN.	12	V/ μs
t_{on}	$I_G=80mA$ $I_A=400mA$ $I_R=40mA$ $T_j=25$		TYP.	5	μs
t_{off}				50	



Symbol	Parameter	Value(MAX.)	Unit	
V_{TM}	$I_{TM}=22.5A$ $t_p=380\mu s$ $T_j=25$	1.5	V	
V_{TO}	Threshold voltage $T_j=125$	0.77	V	
R_D	Dynamic resistance $T_j=125$	30	m	
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25$	5	μA
I_{RRM}		$T_j=125$	0.5	mA



Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	1	/W
$R_{th(j-a)}$	junction to ambient (AC)	60	/W

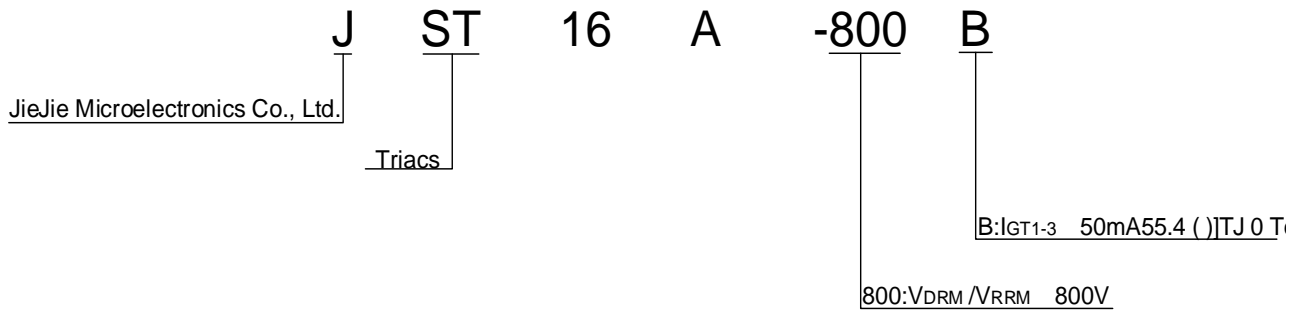


FIG.1: Maximum power dissipation versus RMS on-state current

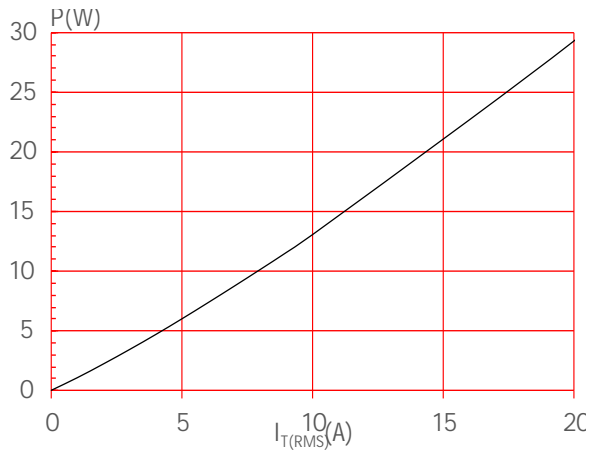
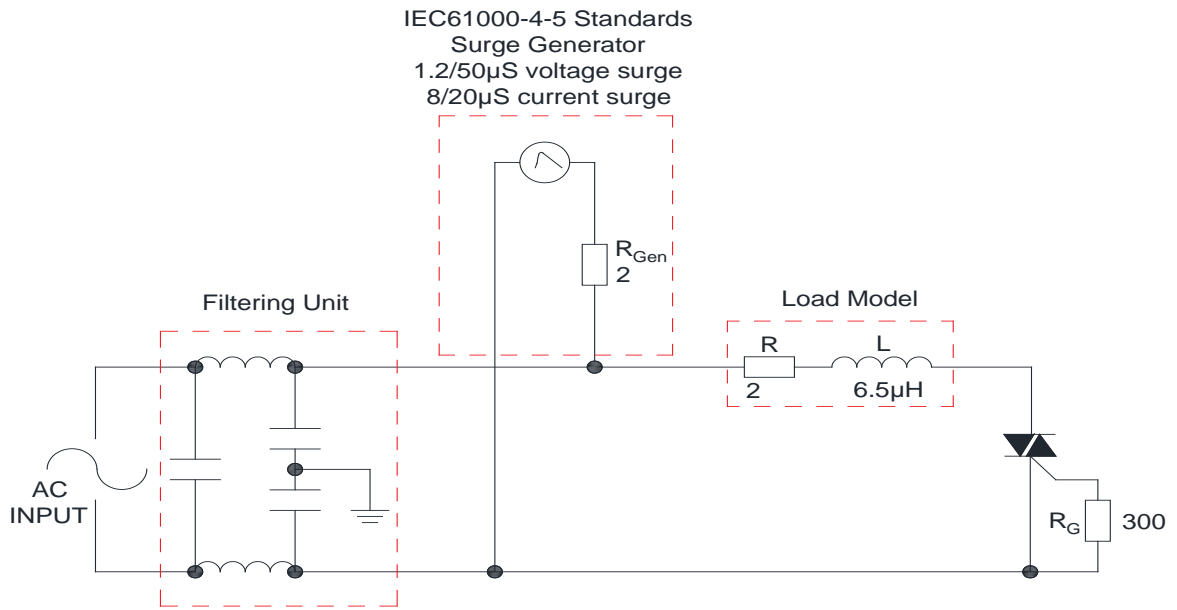
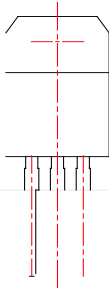


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Refer to the application note Assembly Instructions for Thyristors in Through-hole Package released by JieJie Microelectronics



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