



JST139F-800E 16A TRIAC

Rev.A.1.1

DESCRIPTION:

The JST139F-800E triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. By using an external plastic package, JST139F-800E provides a rated insulation voltage of 2000 VRMS, complying with UL standards (File ref: E252906). Package TO-220F is RoHS compliant.

MAIN FEATURES

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	800	V
RMS on-state current ($T_c 070^\circ\text{C}$)	$I_{T(RMS)}$	16	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	140	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		154	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	98	A^2s
Critical rate of rise of on-state current ($I_G=2\text{H I}_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)	-	80	$\text{A}/\mu\text{s}$
	-	50	
Peak gate current ($t_p=20\mu\text{s}$, $T_j=125^\circ\text{C}$)	I_{GM}	4	A
Average gate power dissipation ($T_j=125^\circ\text{C}$)	$P_{G(AV)}$	0.5	W

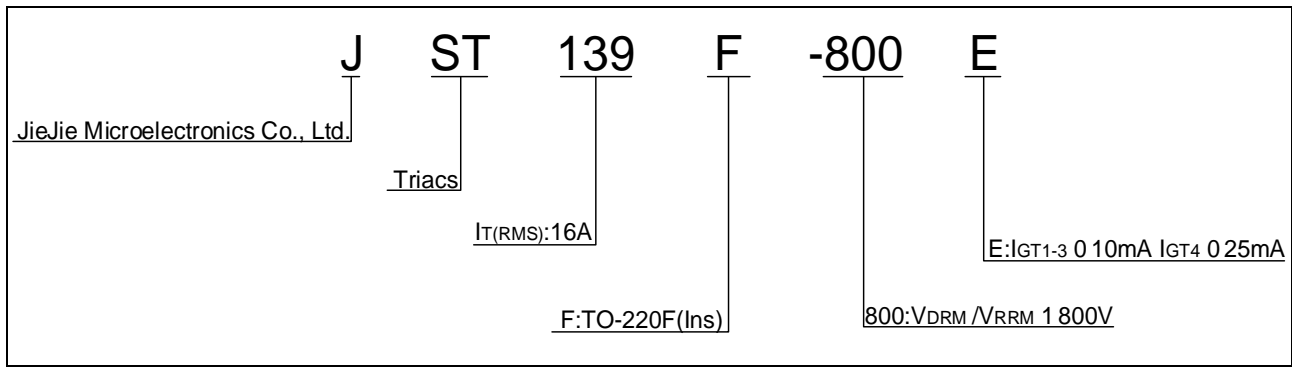


Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	4.5	kV
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ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33$	- -	MAX.	10	mA
				2.5	
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	- -	MAX.	25	mA
				3.5	
I_H	$I_T=500mA$		MAX.	25	mA
dV/dt	$V_D=540V$ Gate Open TdV.			D	MAO

ORDERING INFORMATION



MARKING

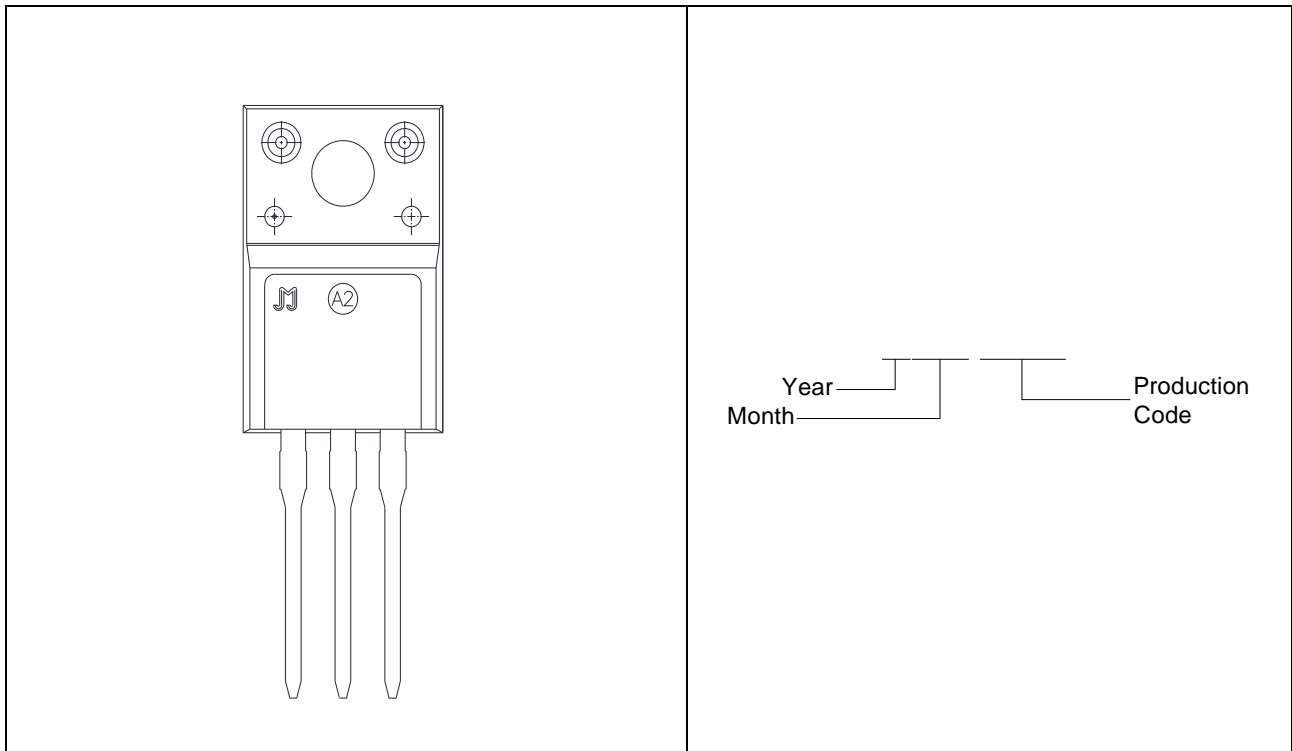


FIG.1: Maximum power dissipation versus RMS on-state current

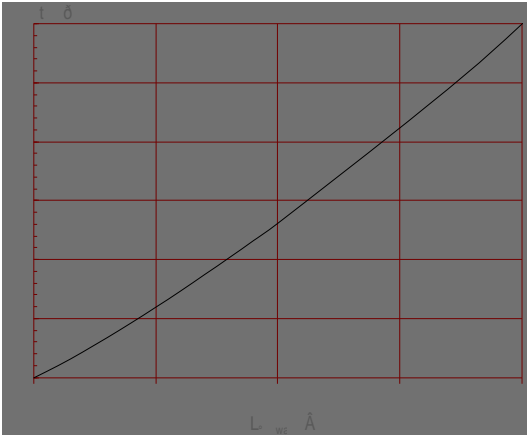


FIG.2: RMS on-state current versus case temperature

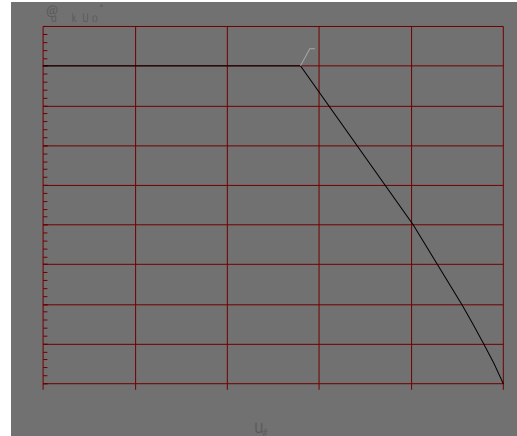


FIG.3: Surge peak on-state current versus number of cycles

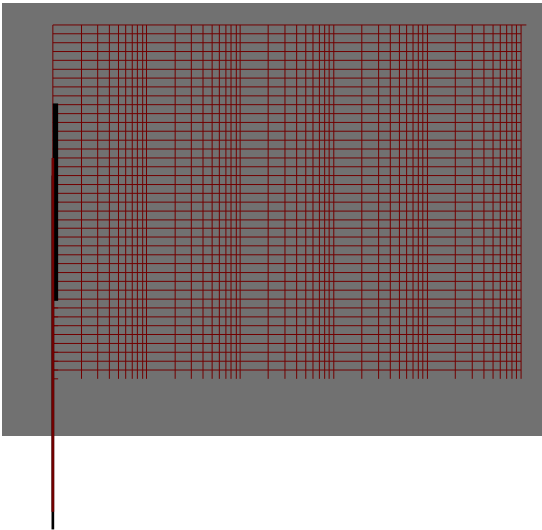
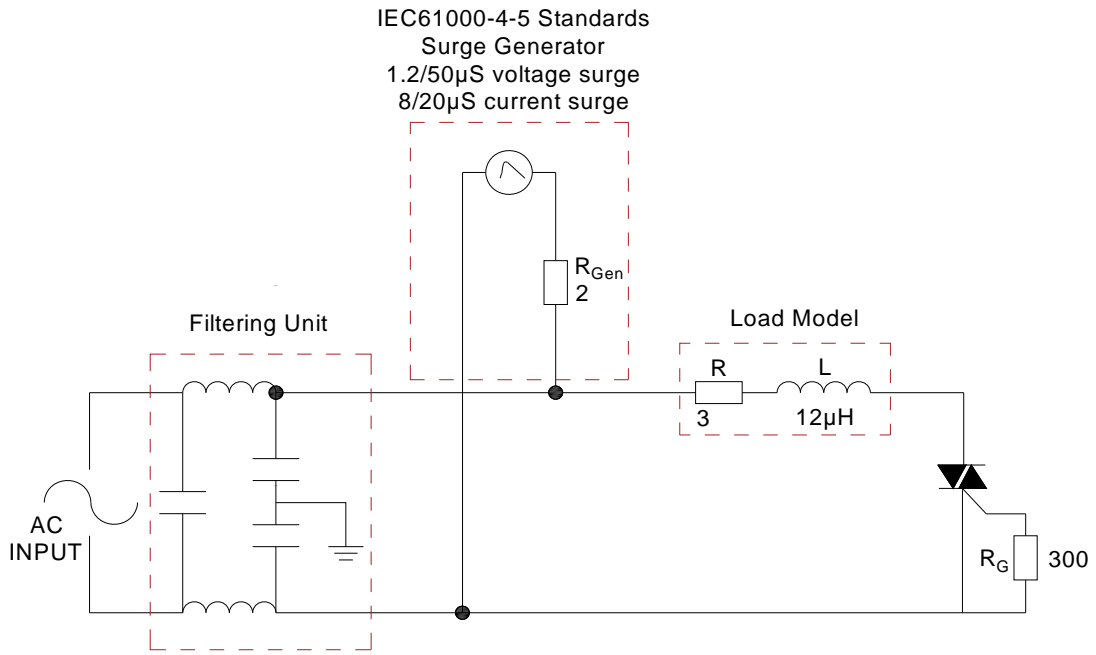


FIG.4: On-state characteristics

FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards





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