

JIEJIE MICROELECTRONICS CO., LTD.

(T_j=25 °C)

Symbol	Test Condition	Quadrant	Value		Unit
I _{GT}	V _D =12V R _L =33	- -	MAX.	5	A
				10	
V _{GT}		ALL	MAX.	1	V
V _{GD}	V _D =V _{DRM} T _j =125 R _L =3.3k	ALL	MIN.	0.2	V
I _L	I _G =1.2I _{GT}	- -	MAX.	10	A
				20	
I _H	I _T =100A		MAX.	7	A
V _d	V _D =540V Gate Op n _{Tj} =110		MIN.	100	V

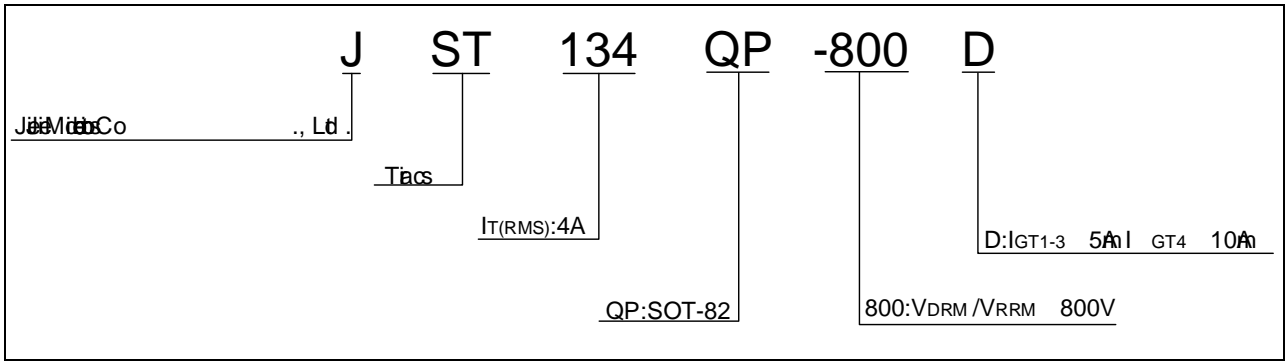
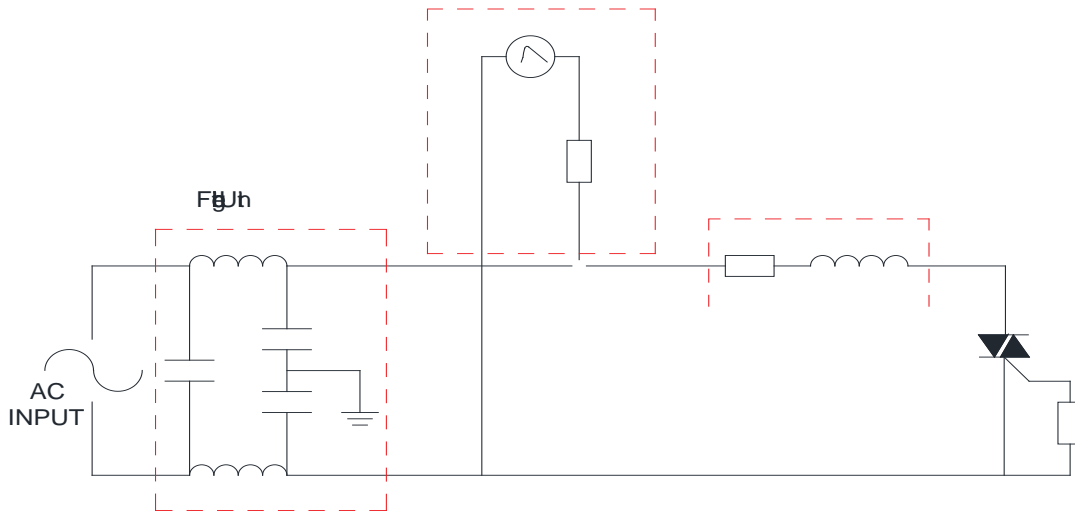


FIG.1: Main RMS

0 10 20



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
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JST134QP-800D

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