

| | | | |
|--|----------|-----|----|
| Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7) | V_{PP} | 3.5 | kV |
|--|----------|-----|----|

ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

| Symbol | Test Condition | Quadrant | Value | | Unit |
|----------|---------------------------------------|----------|-------|-----|------|
| I_{GT} | $V_D=12V$ $R_L=33$ | - - | MAX. | 10 | mA |
| | | | | 25 | |
| V_{GT} | | ALL | MAX. | 1.3 | V |
| V_{GD} | $V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k$ | ALL | MIN. | 0.2 | V |
| I_L | $I_G=1.2I_{GT}$ | - - | MAX. | 15 | mA |
| | | | | 30 | |

I ■

JST131U-600E

JieJie Microelectronics Co., Ltd.

FIG.1: Maximum power dissipation versus RMS on-state current

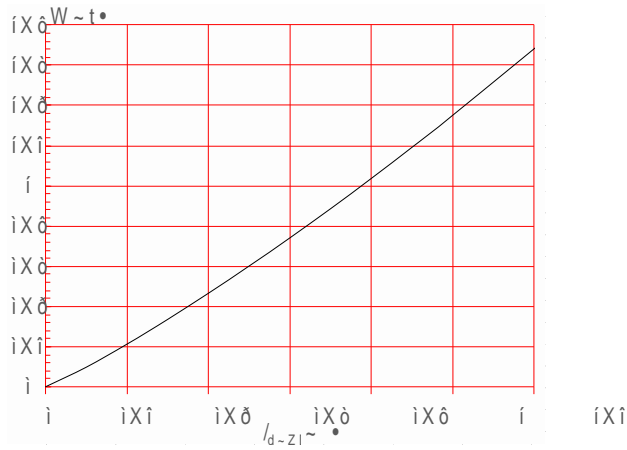


FIG.2: RMS on-state current versus case temperature

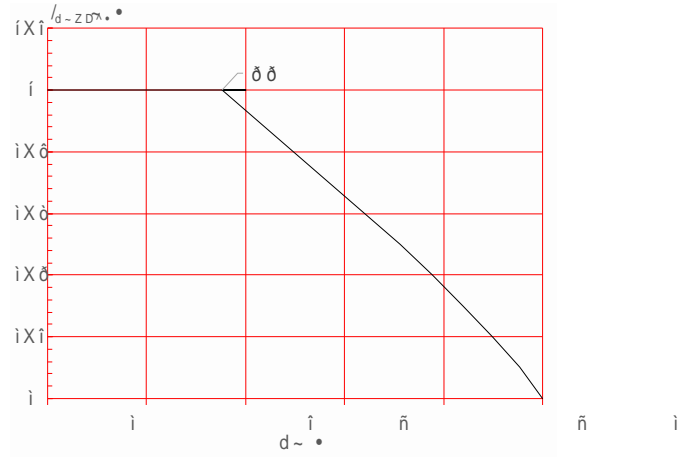


FIG.3: Surge peak on-state current versus number of cycles

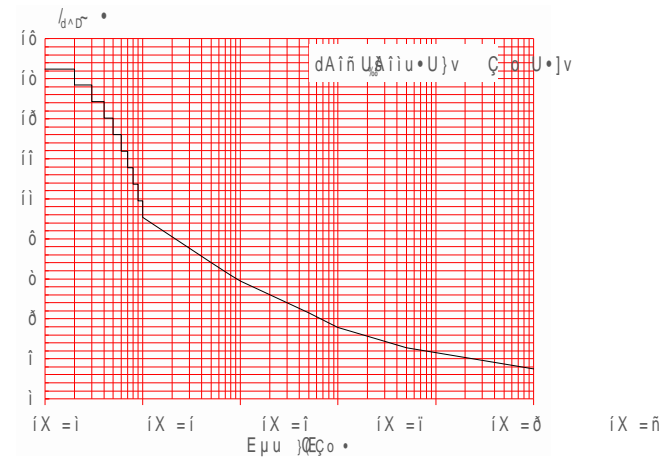


FIG.4: On-state characteristics

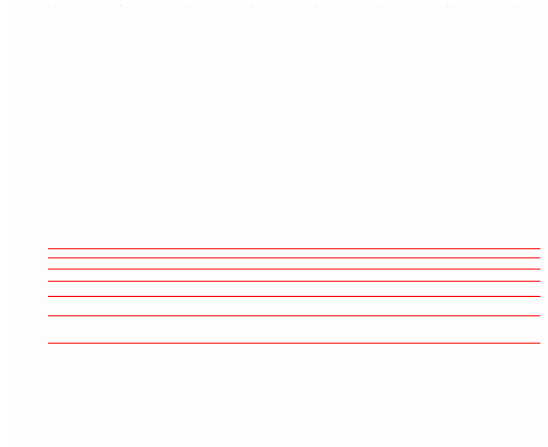
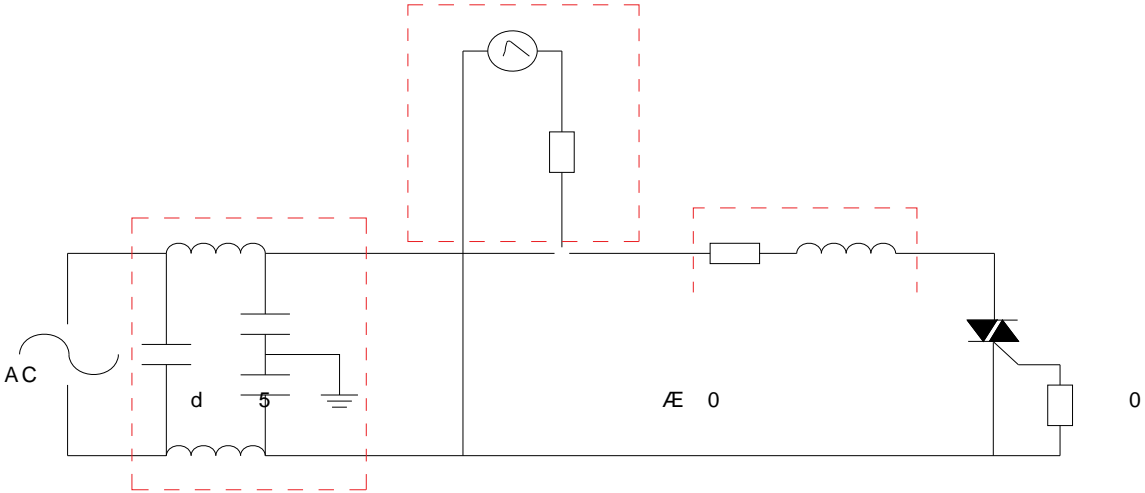
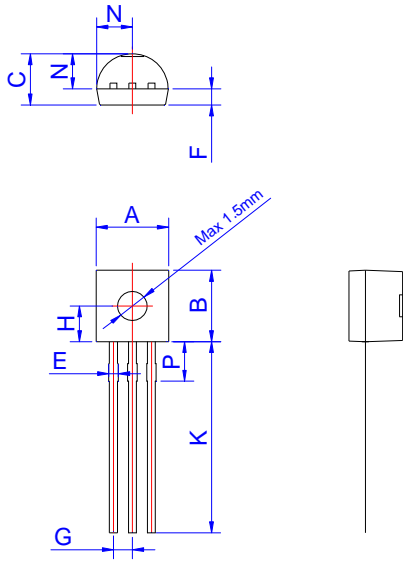


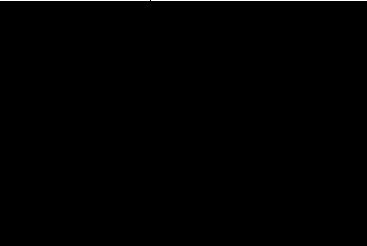
FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



PACKAGE MECHANICAL DATA



| | | | | |
|------------|------------|------------|------------|------------|
| [Redacted] | | [Redacted] | | [Redacted] |
| [Redacted] | [Redacted] | [Redacted] | [Redacted] | [Redacted] |



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is