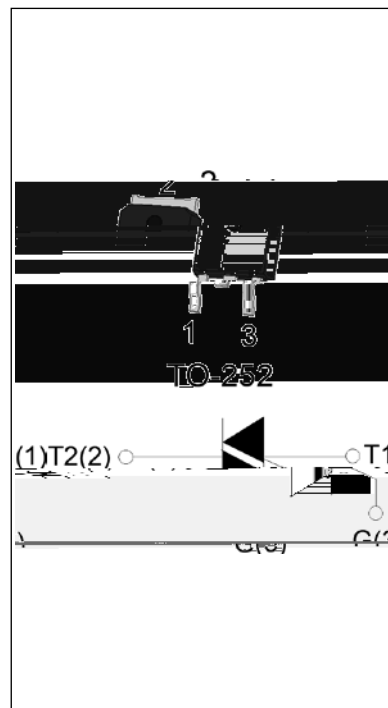


JST08K-800BW 8A TRIAC

Rev.A.1.2

DESCRIPTION:

The JST08K-800BW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST08K-800BW snubberless triac is especially recommended for use on inductive loads. Package TO-252 is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	8	A
V_{DRM}/V_{RRM}	800	V
$I_{GT} / /$	50/50/50	mA

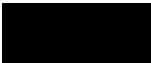
ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	800	V
RMS on-state current ($T_c = 091^\circ\text{C}$)	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	80	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		88	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	32	A^2s
$\times$	di/dt	100	A/s
Peak gate current ($t_p=20\text{ s}$, $T_j=125^\circ\text{C}$)	I_{GM}	4	A
Average gate power dissipation ($T_j=125^\circ\text{C}$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W

Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.8)	V_{pp}	2.5	kV
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ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

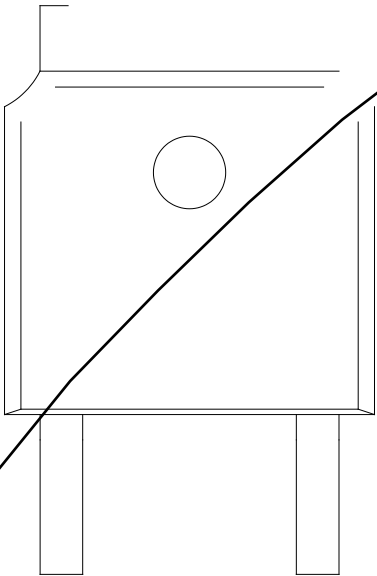
Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33$	- -	MAX.	50	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k$	- -	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	50	mA
				90	
I_H	$I_T=100mA$		MAX.	50	mA
dV/dt	$V_D=540V$ Gate Open $T_j=125$		MIN.	5000	V s



ORDERING INFORMATION

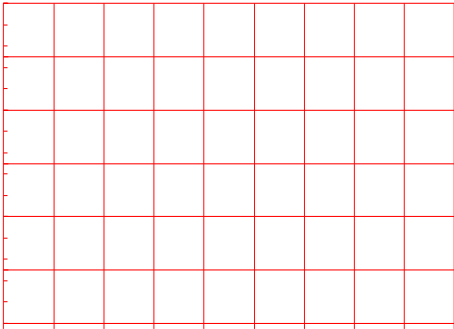
J	ST	08	K	-800	BW	-/
JieJie Microelectronics Co., Ltd.	Triacs	IT(RMS):8A				Blank:Tube -TR:Tape & Reel
		K:TO-252			BW:IGT1-3 50mA	
				800:VDRM /VRRM	800V	

MARKING



JST08K-800BW

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature



ORDERING INFORMATION

Order code	Voltage V _{DRM} /V _{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
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DELIVERY MODE

