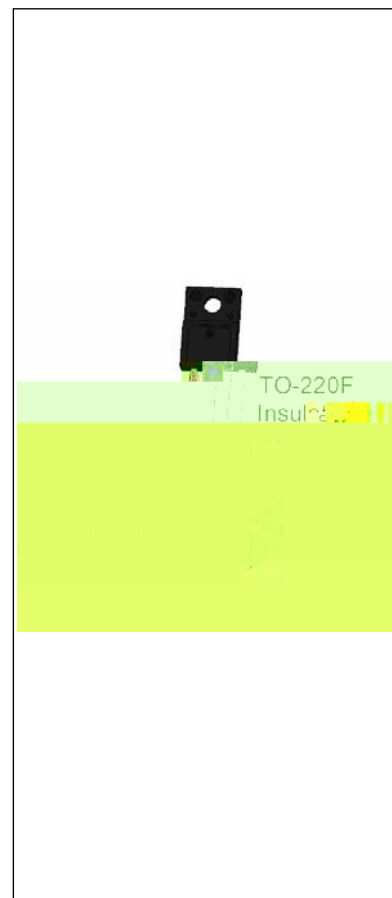


**DESCRIPTION:**

The JST08F-600SW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST08F-600SW snubberless triac is especially recommended for use on inductive loads. It can be driven directly through the MCU I/O port. By using an external plastic package, JST08F-600SW provides a rated insulation voltage of 2000 VRMS, complying with UL standards (File ref: E252906). Package TO-220F is RoHS compliant.


**MAIN FEATURES**

| Symbol            | Value    | Unit |
|-------------------|----------|------|
| $I_{T(RMS)}$      | 8        | A    |
| $V_{DRM}/V_{RRM}$ | 600      | V    |
| $I_{GT} / /$      | 10/10/10 | mA   |

**ABSOLUTE MAXIMUM RATINGS**

| Parameter                                                                                                        | Symbol       | Value   | Unit                   |
|------------------------------------------------------------------------------------------------------------------|--------------|---------|------------------------|
| Storage junction temperature range                                                                               | $T_{stg}$    | -40-150 |                        |
| Operating junction temperature range                                                                             | $T_j$        | -40-125 |                        |
| Repetitive peak off-state voltage ( $T_j=25^\circ\text{C}$ )                                                     | $V_{DRM}$    | 600     | V                      |
| Repetitive peak reverse voltage ( $T_j=25^\circ\text{C}$ )                                                       | $V_{RRM}$    | 600     | V                      |
| RMS on-state current ( $T_C 094^\circ\text{C}$ )                                                                 | $I_{T(RMS)}$ | 8       | A                      |
| Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$ , $T_j=25^\circ\text{C}$ )             | $I_{TSM}$    | 80      | A                      |
| Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$ , $T_j=25^\circ\text{C}$ )           |              | 88      |                        |
| $I^2t$ value for fusing ( $t_p=10\text{ms}$ , $T_j=25^\circ\text{C}$ )                                           | $I^2t$       | 32      | $\text{A}^2\text{s}$   |
| Critical rate of rise of on-state current ( $I_G=2\text{hI}_{GT}$ , $f=100\text{Hz}$ , $T_j=125^\circ\text{C}$ ) | $di/dt$      | 50      | $\text{A}/\mu\text{s}$ |

|                                                                    |             |     |    |
|--------------------------------------------------------------------|-------------|-----|----|
| Peak gate current ( $t_p=20\mu s$ , $T_j=125$ )                    | $I_{GM}$    | 4   | A  |
| Average gate power dissipation ( $T_j=125$ )                       | $P_{G(AV)}$ | 0.5 | W  |
| Peak gate power                                                    | $P_{GM}$    | 10  | W  |
| Peak pulse voltage<br>( $T_j=25$ ; non-repetitive,off-state;FIG.7) | $V_{pp}$    | 1.5 | kV |

ELECTRICAL CHARACTERISTICS ( $T_j=25$  unless otherwise specified)

| Symbol    | Test Condition                                | Quadrant | Value |     | Unit       |
|-----------|-----------------------------------------------|----------|-------|-----|------------|
| $I_{GT}$  | $V_D=12V$ $R_L=33\Omega$                      | - -      | MAX.  | 10  | mA         |
| $V_{GT}$  |                                               | - -      | MAX.  | 1   | V          |
| $V_{GD}$  | $V_D=V_{DRM}$ $T_j=125$<br>$R_L=3.3k\Omega$   | - -      | MIN.  | 0.2 | V          |
| $I_L$     | $I_G=1.2I_{GT}$                               | -        | MAX.  | 25  | mA         |
|           |                                               |          |       | 30  |            |
| $I_H$     | $I_T=100mA$                                   |          | MAX.  | 15  | mA         |
| dV/dt     | $V_D=400V$ Gate Open $T_j=125$                |          | MIN.  | 500 | V/ $\mu s$ |
| (dI/dt)c  | (dV/dt)c=10V/ $\mu s$ , $T_j=125$             |          | MIN.  | 3   | A/ms       |
| $t_{on}$  | $I_G=20mA$ $I_A=200mA$ $I_R=20mA$<br>$T_j=25$ |          | TYP.  | 2.5 | $\mu s$    |
| $t_{off}$ |                                               |          |       | 25  |            |

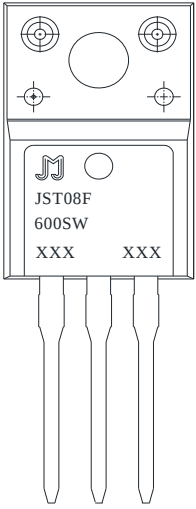
STATIC CHARACTERISTICS

| Symbol   | Parameter                     | Value(MAX.) | Unit |
|----------|-------------------------------|-------------|------|
| $V_{TM}$ | $I_{TM}=11A$ $t_p=380\mu s$ T |             |      |

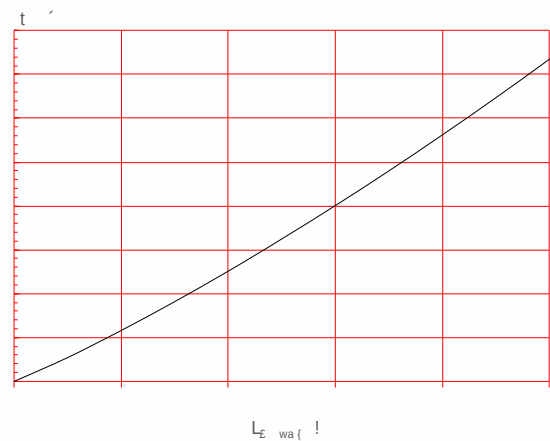
ORDERING INFORMATION

|                                   |        |            |                |                       |                  |
|-----------------------------------|--------|------------|----------------|-----------------------|------------------|
| J                                 | ST     | 08         | F              | -600                  | SW               |
| JieJie Microelectronics Co., Ltd. | Triacs | IT(RMS):8A | F:TO-220F(Ins) |                       | SW:IGT1-3 0.10mA |
|                                   |        |            |                | 600:VDRM /VRRM 1 600V |                  |

MARKING

|                                                                                     |                                                                         |
|-------------------------------------------------------------------------------------|-------------------------------------------------------------------------|
|  | <div>XXX XXX</div> <div>Year<br/>Month</div> <div>Production Code</div> |
|-------------------------------------------------------------------------------------|-------------------------------------------------------------------------|

**FIG.1:** Maximum power dissipation versus RMS on-state current



**FIG.2:** RMS on-state current versus case temperature

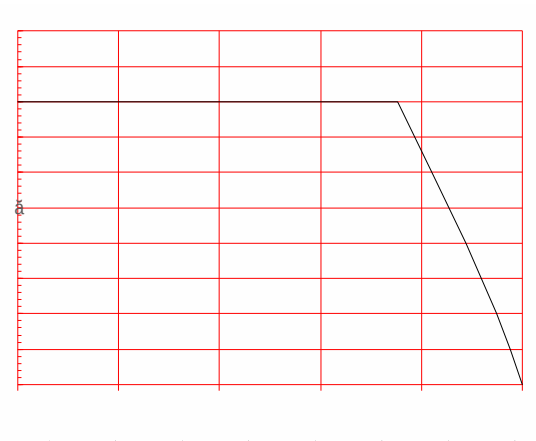
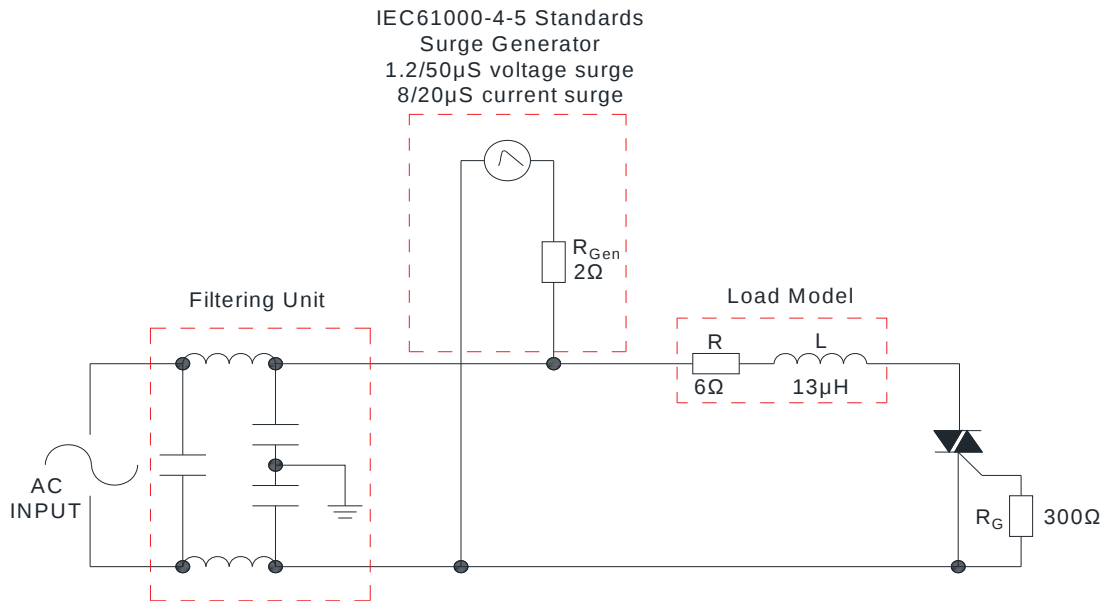


FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



## LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie aΩ Ő o ů Ş P Ş Ő ú o ů Ω'Ω Ő σ

## ORDERING INFORMATION

| Order code   | Voltage<br>$V_{\text{DRM}}/V_{\text{RRM}}$ (V) | IGT(mA) | Package      | Base qty.<br>(pcs) | Delivery<br>mode |
|--------------|------------------------------------------------|---------|--------------|--------------------|------------------|
|              |                                                | - -     |              |                    |                  |
| JST08F-600SW | 600                                            | 10      | TO-220F(Ins) | 50                 | Tube             |

## Document Revision History

| Date         | Revision | Changes                        |
|--------------|----------|--------------------------------|
| Apr.11, 2023 | A.1.0    | Last updated                   |
| Oct.13, 2025 | A.1.1    | Revise PACKAGE MECHANICAL DATA |

PACKAGE MECHANICAL DATA



