



**JST04K-600SW 4A TRIAC**

|                                                                |     |   |    |
|----------------------------------------------------------------|-----|---|----|
| Peak pulse voltage<br>(Tj=25 ; non-repetitive,off-state;FIG.8) | Vpp | 3 | kV |
|----------------------------------------------------------------|-----|---|----|

ELECTRICAL CHARACTERISTICS (Tj=25 unless otherwise specified)

Symbol

Test Condition

QuadrantonSpan]TJ 0 Tc 21

ORDERING INFORMATION

|                                   |        |            |   |                |                |                               |
|-----------------------------------|--------|------------|---|----------------|----------------|-------------------------------|
| J                                 | ST     | 04         | K | -600           | SW             | -/                            |
| JieJie Microelectronics Co., Ltd. | Triacs | IT(RMS):4A |   |                |                | Blank:Tube<br>-TR:Tape & Reel |
|                                   |        | K:TO-252   |   |                | SW:IGT1-3 10mA |                               |
|                                   |        |            |   | 600:VDRM /VRRM | 600V           |                               |

**FIG.1:** Maximum power dissipation versus RMS on-state current



**FIG.2:** RMS on-state current versus case temperature

**FIG.7:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

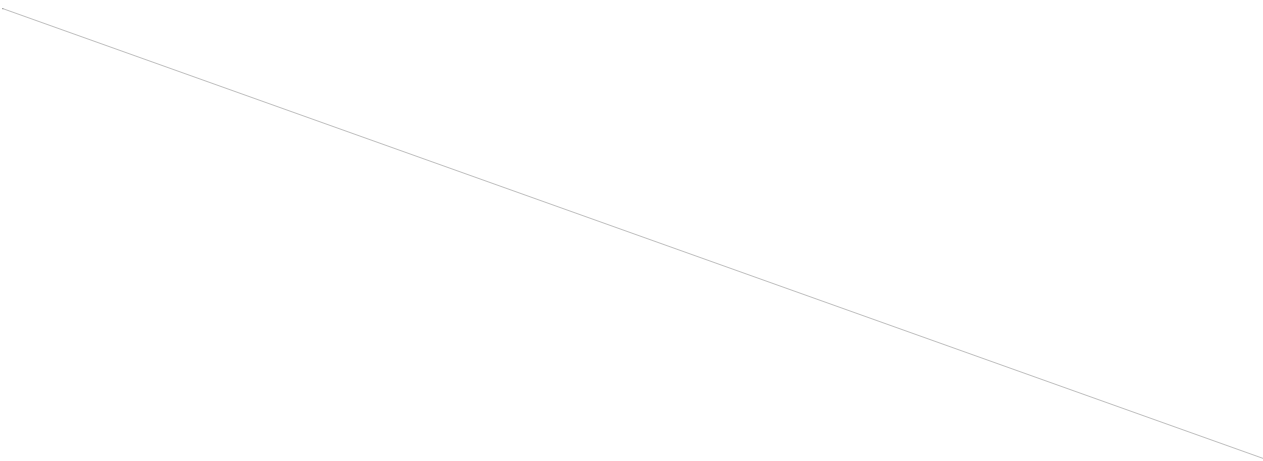
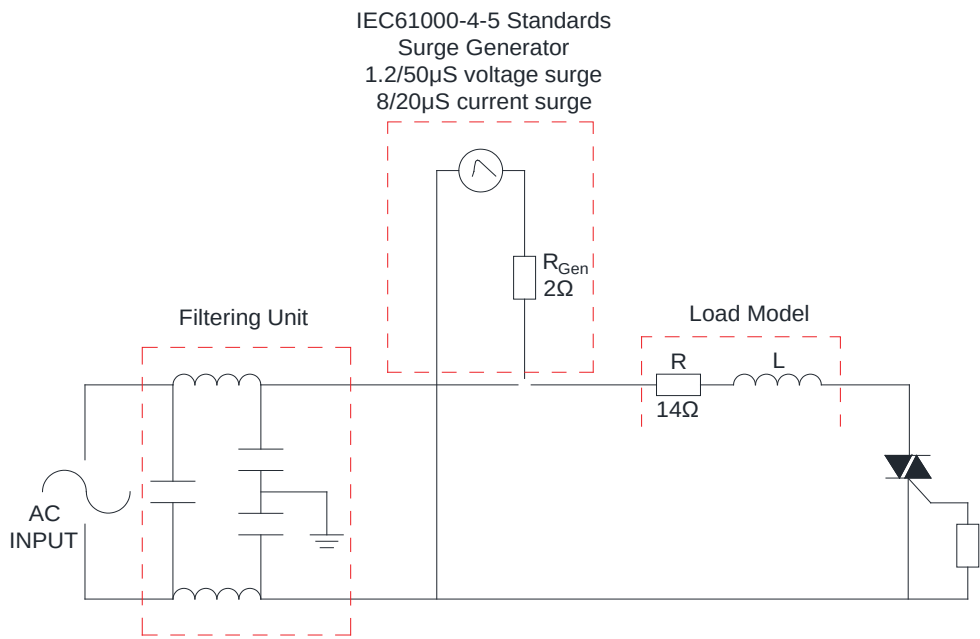


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



OREaEm[OR)-I,m[OR)-M5

Eq. 0.03010-  
B2  
H

[illegible]





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