

NOTE 1: Operating junction temperature T_j is up to 125 when a resistor $\leq 1k\Omega$ is connected between Gate and Cathode. Without this resistor, the T_j is up to 110 only.

Symbol	Test Condition	Value	Unit
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ORDERING INFORMATION

<u>J</u>	<u>R</u>	<u>08</u>	<u>05</u>	<u>A</u>
<u>JieJie Microelectronics Co., Ltd.</u>	<u>Sensitive gate SCRs</u>	<u>IT(RMS):8A</u>		

FIG.1: Maximum power dissipation versus RMS on-state current

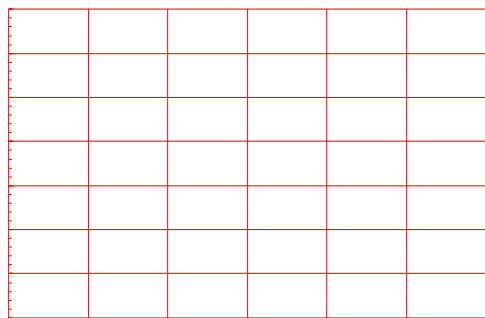
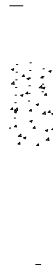
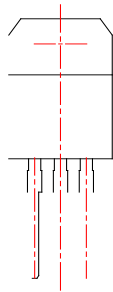


FIG.2: RMS on-state current versus case temperature

PACKAGE MECHANICAL DATA



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