





## ORDERING INFORMATION

|                                   |           |                                |                   |               |
|-----------------------------------|-----------|--------------------------------|-------------------|---------------|
| <u>J</u>                          | <u>CT</u> | <u>8</u>                       | <u>55</u>         | <u>Z</u>      |
| JieJie Microelectronics Co., Ltd. | SCRs      | 8: $V_{DRM}/V_{RRM} \geq 800V$ | $I_{T(RMS)}: 55A$ | Z: TO-3P(Ins) |

## MARKING

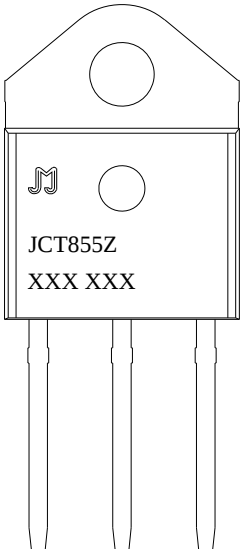
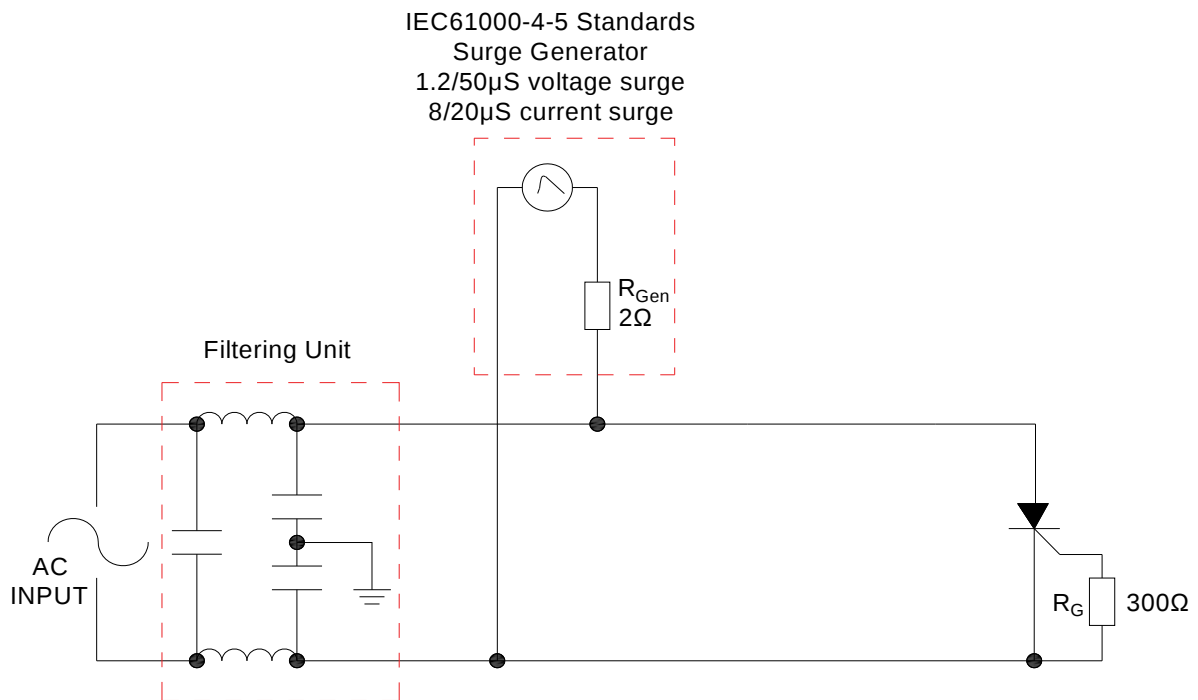
|                                                                                     |                                                                                                          |
|-------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------|
|  | <div> <div>XXX XXX</div> <div> <div>Year</div> <div>Month</div> </div> <div>Production Code</div> </div> |
|-------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------|



FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



## LEAD FORMING AND SOLDERING

Refer to the application note "Assembly Instructions for Thyristors in Through-hole Package" released by JieJie Microelectronics



**JCT855Z**

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**PACKAGE MECHA**

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