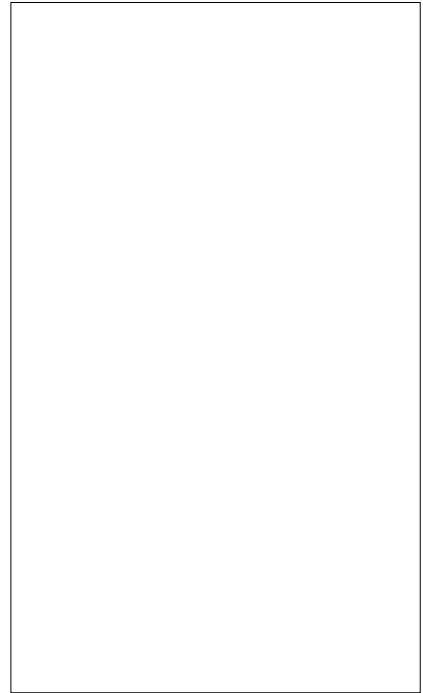
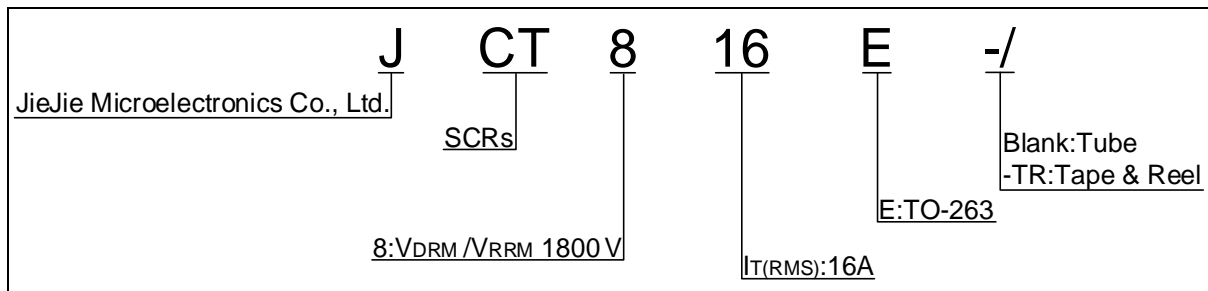




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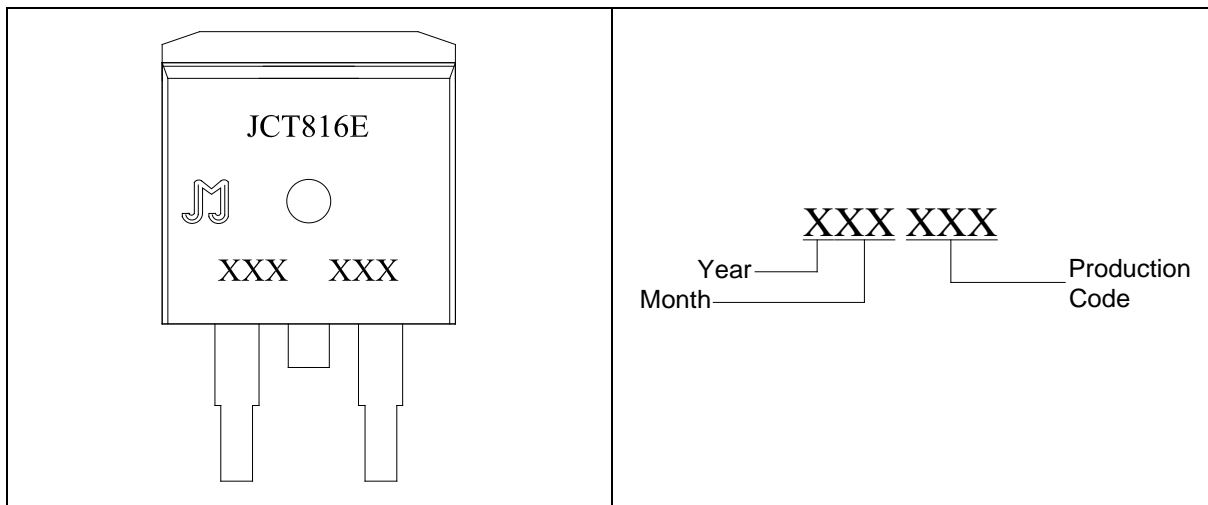


FIG.1: Maximum power dissipation versus RMS on-state current

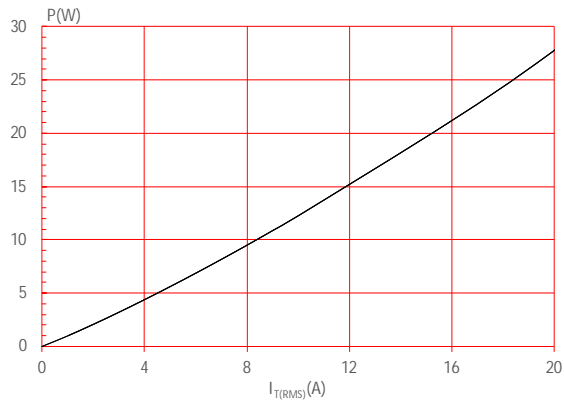


FIG.2: RMS on-state current versus case temperature

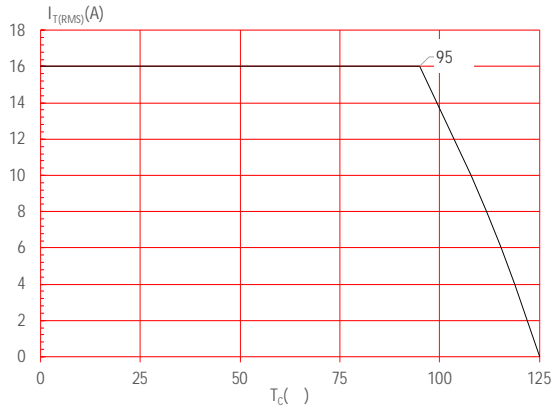


FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper thickness:35 μ m)(full cycle)

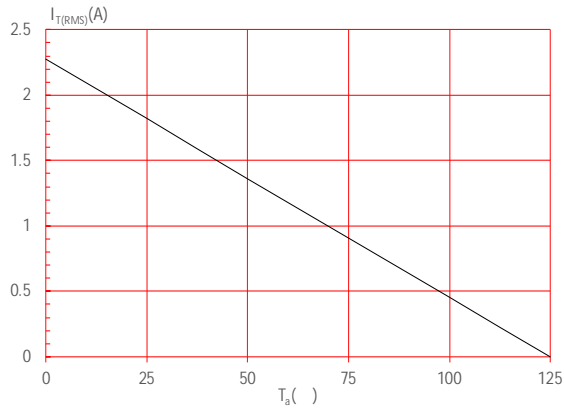
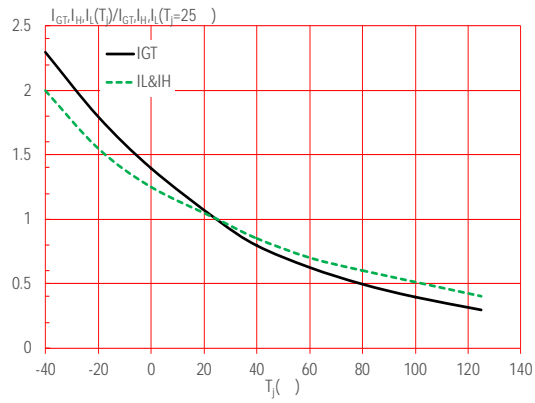


FIG.4: Surge peak on-state current versus number of cycles

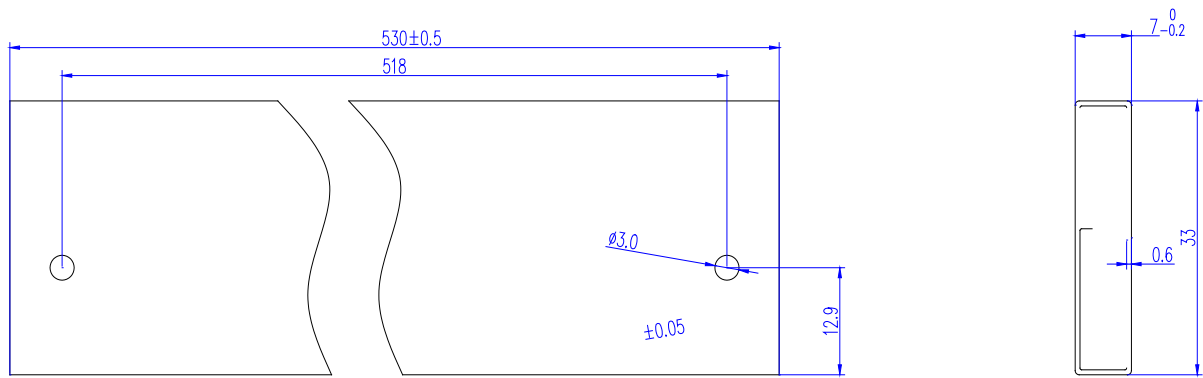
FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature



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