



Average gate power dissipation ($T_j=125$) P

FIG.1: Maximum power dissipation versus RMS on-state current

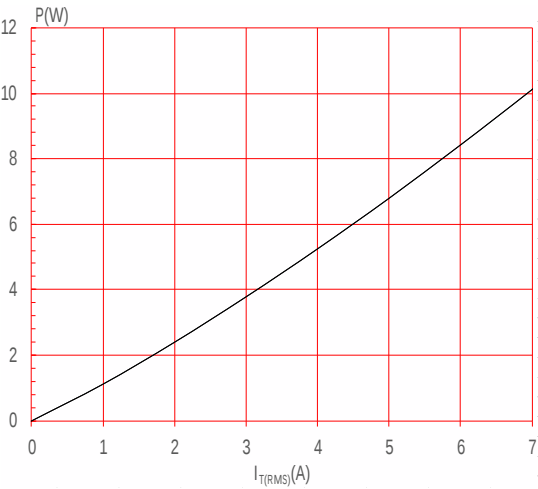
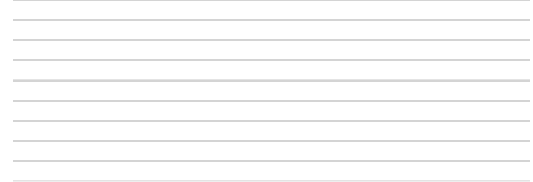


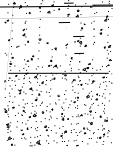
FIG.2: RMS on-state current versus case temperature



Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
ACJT625-10F	1000	25	TO-220F(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.14, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA



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